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DM	2 826 814	01/2003	French		_	Abstract	
DM	1 117 271	07/2001	ЕРО				
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BM	Draft Standard for a Control and Status Registers (CSR) Architecture for microcomputer buses, Microprocessor and Microcomputer Standards Committee of the IEEE Computer Society; P1212 Draft 1.0 October 18, 1999, pages i to ix and 1 to 64.						
DM	IEEE Standar Committee of 88 and 234 to	the IEEE Compu	formance Serial Bus, Microprocessor and iter Society, IEEE Standards Board, Deco	Microcon ember 12,	nputer Sta 1995, page	ndards s i, 48, 87,	
DM	IEEE Standard for a High Performance Serial Bus, pages 1 to 3, 19 to 21, 40 to 43, 100, 101 and 127 (1996).				and 127		
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^{*}EXAMINER: Initial If reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation If not in conformance and not considered. Include copy of this form with next communication to applicant.

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